

13 layer is subjected to an aligning treatment and a direction in which said second  
14 alignment layer is subjected to an aligning treatment is set to a value of 0.5 to 4.0  
15 degrees, in order to decrease a maximum voltage between the pixel electrode and the  
16 common electrode, and to increase a response of switching said liquid crystal while a  
17 high contrast ratio is sustained.

2. (Amended) The active matrix type liquid crystal display device according to claim 1,  
wherein said angle made between said direction in which said first alignment layer is  
subjected to said aligning treatment and said direction in which said second alignment layer is  
subjected to said aligning treatment is set to a value of 1.5 to 2.0 degrees, in order to decrease  
the maximum voltage between the pixel electrode and the common electrode and to increase  
the response of switching said liquid crystal while the high contrast ratio is sustained.

3. (Amended) The active matrix type liquid crystal display device according to claim 1,  
wherein said direction in which said first alignment layer is subjected to said aligning  
treatment has an angle of 5 to 45 degrees with respect to a parallel direction in which said  
common electrode and said pixel electrode are wired in parallel with each other.

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**Please add the following new claims:**

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-- 10. An active matrix type liquid crystal display device, comprising:

- a first substrate;
- a first alignment layer formed on a surface of said first substrate;
- a second substrate opposing said first substrate;
- a second alignment layer formed on said second substrate;
- a liquid crystal chain held between said first alignment layer and said second  
alignment layer,

wherein, in the absence of a potential difference, a first end of said liquid crystal  
contacting said first alignment layer is rotated to form a first angle relative to a reference  
point on said first substrate, and a second end of said liquid crystal is rotated to form a second  
angle relative to the reference point on said first substrate.

1 11. The device according to claim 10, wherein an absolute value between said first angle  
2 and said second angle is in a range between about 0.5 to about 4.0 degrees.

1 12. The device according to claim 10, wherein an absolute value between said first angle  
2 and said second angle is in a range between about 1.5 to about 2.0 degrees.

1 13. The device according to claim 10, wherein said first angle made from said first alignment  
2 layer subjected to an aligning treatment has an angle in a range between about 5 to about 45  
3 degrees with respect to said reference point on said first substrate.

1 14. The device according to claim 10, further comprising:  
2 a common wiring and a source/drain wiring formed on said first substrate; and  
3 a common electrode and a pixel electrode formed as parts of said common wiring and  
4 said source/drain wiring,  
5 wherein said common electrode and said pixel electrode are wired in parallel with  
6 each other.

1 15. The device according to claim 10, wherein a black display is provided in the absence of  
2 the potential difference.

1 16. The device according to claim 10, wherein light transmittance occurs in the absence of  
2 the potential difference.

1 17. The device according to claim 15, wherein light transmittance occurs in said black  
2 display.

1 18. A method of producing an active matrix type liquid crystal display device, comprising:  
2 forming a thin film transistor (TFT) substrate having a common wiring and a  
3 source/drain wiring on a first substrate;  
4 covering the common wiring and the source/drain wiring with an insulating film;

5 coating the insulating film with a first alignment layer;  
6 forming an opposite substrate opposing the TFT substrate and comprising a second  
7 alignment layer formed on a second substrate;  
8 holding a liquid crystal between the first alignment layer and the second alignment  
9 layer;  
10 forming as parts of the common wiring and the source/drain wiring a common  
11 electrode and a pixel electrode;  
12 wiring the common electrode and the pixel electrode in parallel with each other;  
13 subjecting the first alignment layer to an aligning treatment; and  
14 subjecting the second alignment layer to an aligning treatment,  
15 wherein an angle made between a direction in which the first alignment layer is  
16 subjected to the aligning treatment and a direction in which the second alignment layer is  
17 subjected to the aligning treatment is set to a value of 0.5 to 4.0 degrees,  
18 said angle for decreasing a maximum voltage between the pixel electrode and the  
19 common electrode, and increasing a response of switching said liquid crystal while sustaining  
20 a high contrast ratio.

1 19. The method according to claim 18, wherein said angle made between said direction in  
2 which said first alignment layer is subjected to said aligning treatment and said direction in  
3 which said second alignment layer is subjected to said aligning treatment is set to a value of  
4 1.5 to 2.0 degrees,

5 said angle for decreasing a maximum voltage between the pixel electrode and the  
6 common electrode, and increasing a response of switching said liquid crystal while sustaining  
7 a high contrast ratio.

1 20. The method according to claim 18, wherein said direction in which said first alignment  
2 layer is subjected to said aligning treatment has an angle of 5 to 45 degrees with respect to a  
3 parallel direction in which said common electrode and said pixel electrode are wired in  
4 parallel with each other. --

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